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Kornerup, Peter, "A Systolic, Linear-Array Multiplier for a Class of Right-Shift Algorithms," IEEE Transacti ns on Computers, Vol. 43, No. 8, August, 1994, pp 892-898.

OTHER DOCUMENTS

Montgomery, Peter, "Modular Multiplication Without Trial Division," Mathematics of Computation, Vol. 44, No.170, April 1985, pp. 519-521.

**EXAMINER** 

DATE CONSIDERED

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